**EECS 392: FPGA Systems Design Projects – Midterm Report**

**Outline**

**Introduction:** In the introduction, state the goal of your design project and give a brief overview of your approach.

**Design constraints and requirements:** Comment on the key requirements, components, peripherals, etc. Show block diagrams of your implementation.

**Design description:** This section is where you describe your design in detail. Begin with an overview of your design and follow this with sections describing particular sub-systems and features. Describe the data that is passed between functional units? If it is a streaming architecture, analyze the required sampling rate of data between functional units. What optimizations are you using or planning to use to meet design constraints (area, performance, timing, etc).

**Performance/testing:** In this section discuss how you have tested your design in simulation and assessed its performance. Provide some simulation wave diagrams if appropriate to show proper functionality. Don't overdo it though. Point out any simulation errors or problems you are working on.

**Development Plan:** Provide an updated gantt chart and description of where you are at in terms of completion of tasks. What are the next steps? If you have synthesized your design, have you placed it on the board? What is required to complete the design on schedule?